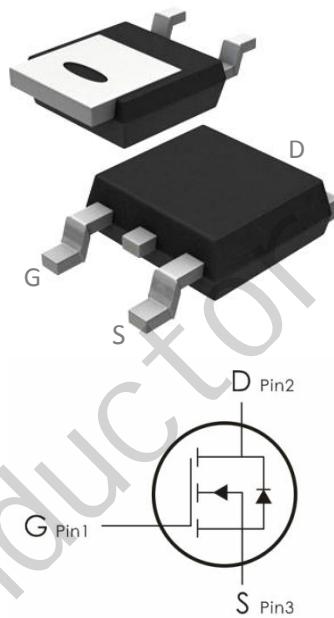


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.



## Features:

- 1)  $V_{DS}=40V, I_D=60A, R_{DS(on)}<7m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.

## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_c=25^\circ C$	60	A
	Continuous Drain Current- $T_c=100^\circ C$	39	A
$I_{DM}$	Pulse Drain Current Tested <sup>note1</sup>	240	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>	81	mJ
$P_D$	Power Dissipation $T_c = 25^\circ C$	47	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case <sup>1</sup>	3.2	°C/W

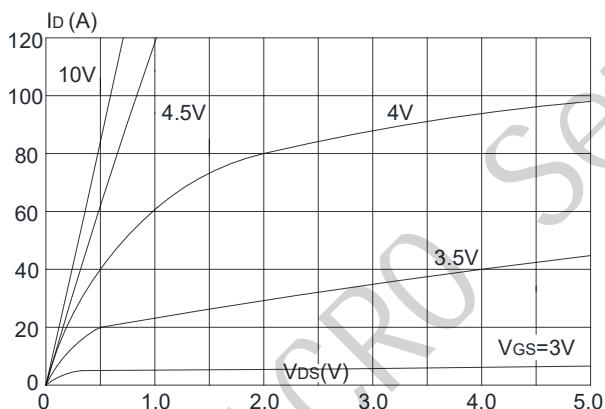
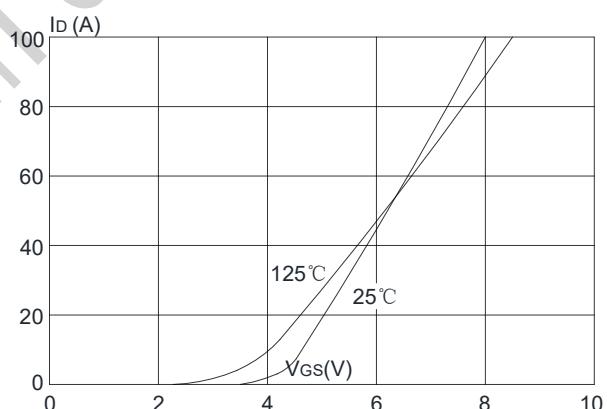
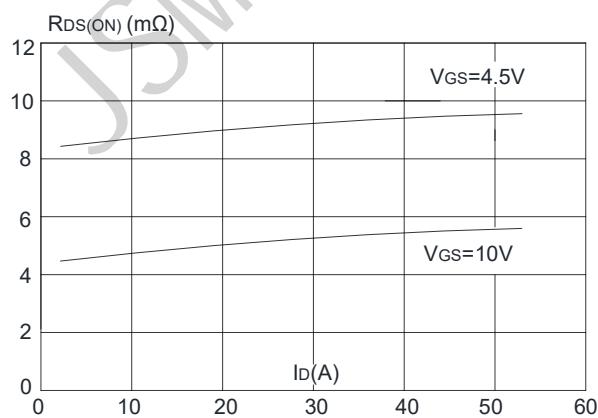
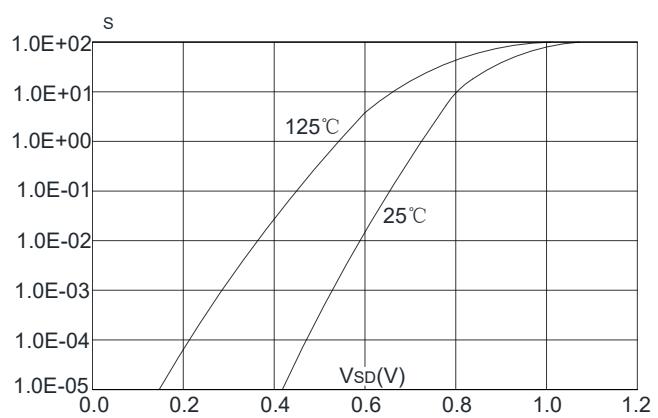
Electrical Characteristics: (T<sub>c</sub>=25°C unless otherwise noted)

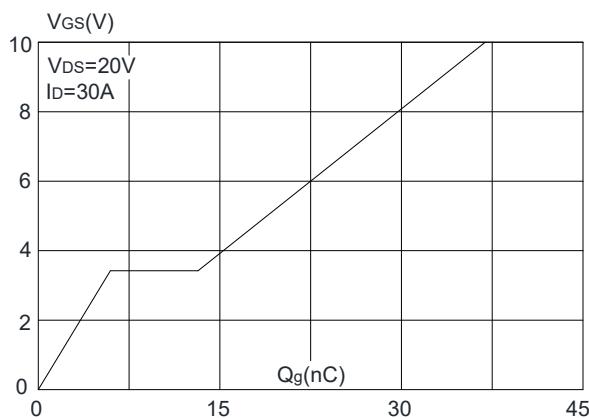
Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
<b>BV<sub>DSS</sub></b>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250 μA	40	---	---	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	---	---	1.0	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0A	---	---	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	GATE-Source Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250 μA	1.0	1.7	2.5	V
R <sub>Ds(on)</sub>	Drain-Source On Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	---	5.5	7	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	---	9	12	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz	---	2380	---	pF
C <sub>oss</sub>	Output Capacitance		---	188	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	160	---	
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	VDD=20V , VGS=10V , RG=3Ω , I <sub>D</sub> =30A	---	10	---	ns
t <sub>r</sub>	Rise Time		---	10	---	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		---	35	---	ns
t <sub>f</sub>	Fall Time		---	7	---	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V, I <sub>D</sub> =30A	---	35	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	5	---	nC
Q <sub>gd</sub>	Gate-Drain "Miller" Charge		---	6	---	nC
<b>Drain-Source Diode Characteristics</b>						
I <sub>S</sub>	Continuous Source Current <sup>1, 4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V ,	---	---	60	A

<b>I<sub>SM</sub></b>	Pulsed Source Current <sup>2~4</sup>	Force Current	---	---	240	A
<b>V<sub>SD</sub></b>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =30A	---	---	1.2	V
<b>T<sub>rr</sub></b>	Reverse Recovery Time	I <sub>F</sub> =20A , dI/dt=100A/μs , T <sub>J</sub> =25°C	---	22	---	ns
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge		---	11	---	nC

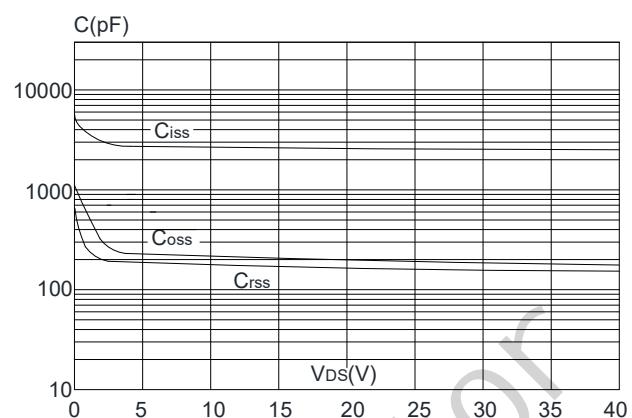
**Notes:**

- 1.Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2.EAS condition: T<sub>J</sub>=25 °C, V<sub>DD</sub>=20V, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH, I<sub>AS</sub>=18A
- 3.Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

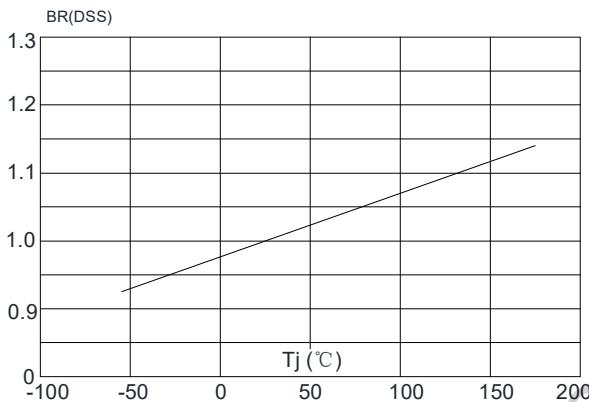
**Typical Characteristics:**

**Figure1:** Output Characteristics

**Figure 2:** Typical Transfer Characteristics

**Figure 3:** On-resistance vs. Drain Current

**Figure 4:** Body Diode Characteristics



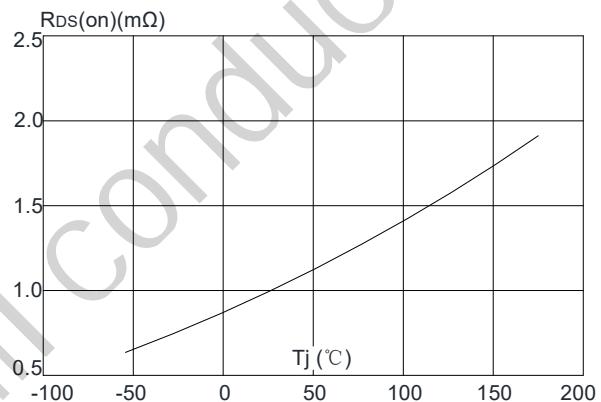
**Figure 5:** Gate Charge Characteristics



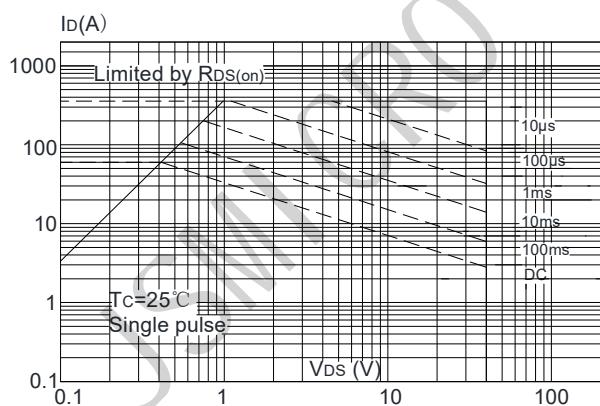
**Figure 6:** Capacitance Characteristics



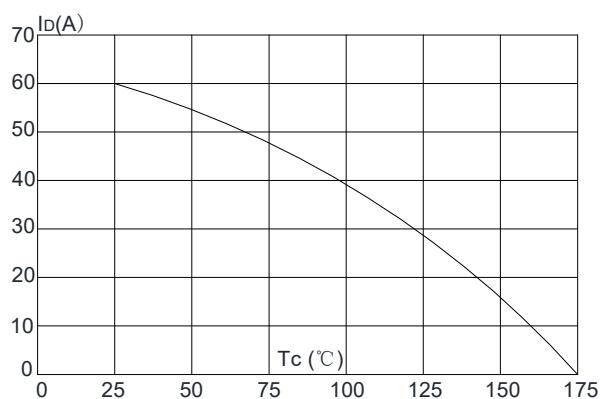
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



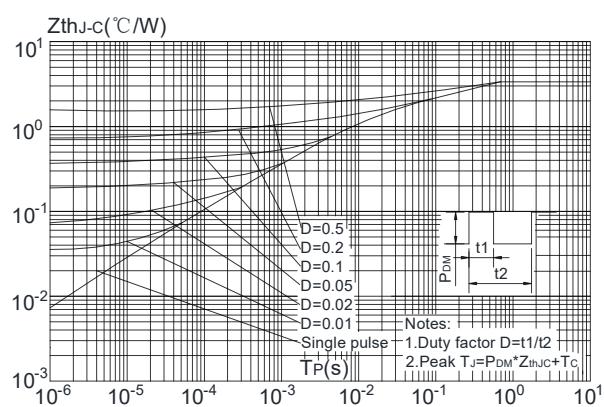
**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective  
Transient Thermal Impedance, Junction-to-Case

## 外形尺寸图 / Package Dimensions

